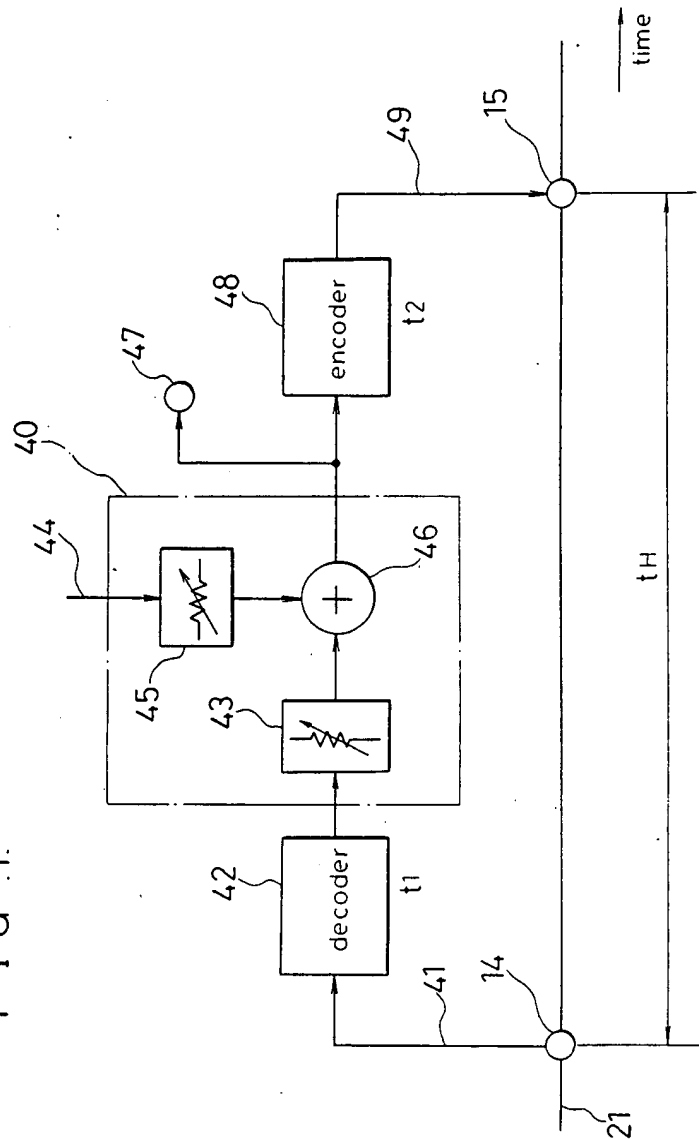


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FIG. 1.

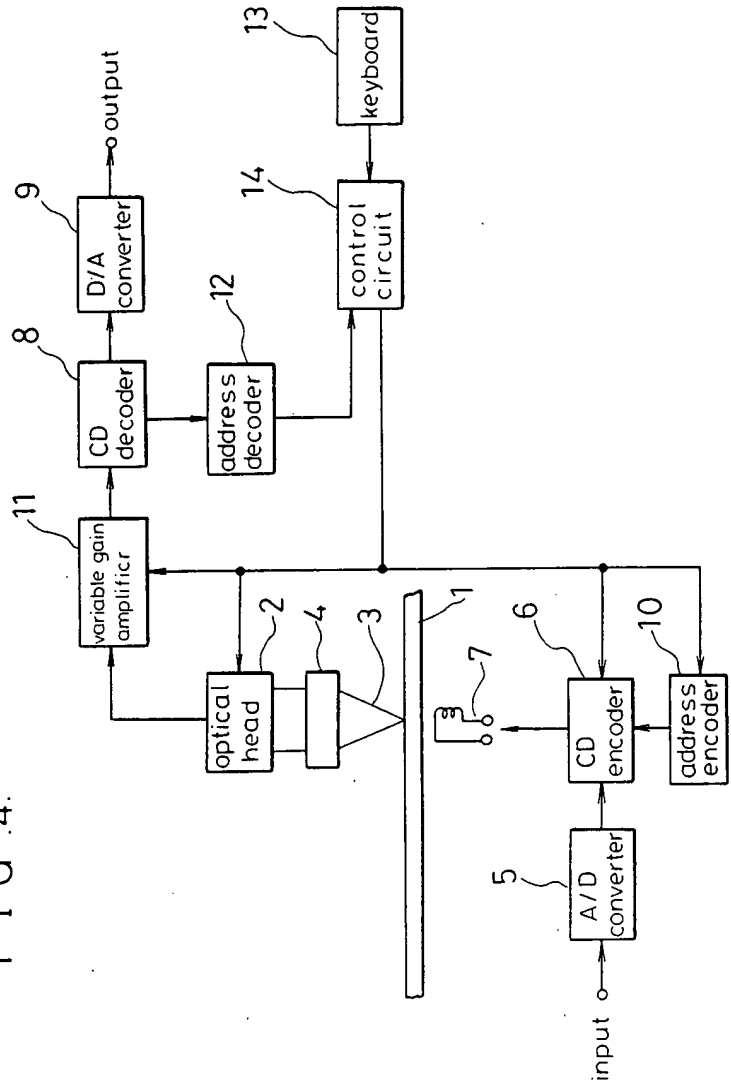


A vertical line with three circles. The circles are labeled 14, 15, and 16 from bottom to top. A bracket labeled 21 is at the bottom. A horizontal line labeled A is at the bottom. A vertical line segment labeled tH is between the circles 14 and 15.

The diagram illustrates a closed-loop control system. A reference input 15 is fed into a summing junction 14 . The output of this junction is signal 16 , which passes through a block 21 to become the system input 41 . This input enters a decoder block 42 (labeled t_1). The output of the decoder is fed into a summing junction 43 (marked with a '+'). The other input to junction 43 is a feedback signal from junction 47 . The output of junction 43 passes through a block 45 (containing a variable resistor symbol) to become signal 44 . This signal 44 is fed into a summing junction 46 (marked with a '+'). The other input to junction 46 is a feedback signal from junction 15 . The output of junction 46 passes through an encoder block 48 (labeled t_2). The output of the encoder is fed into junction 47 and also branches off to junction 15 . A dashed box 40 encloses the blocks 42 , 43 , 45 , and 46 . A horizontal arrow at the bottom indicates the progression of time from t_1 to t_2 .

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FIG. 4.



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FIG. 5.

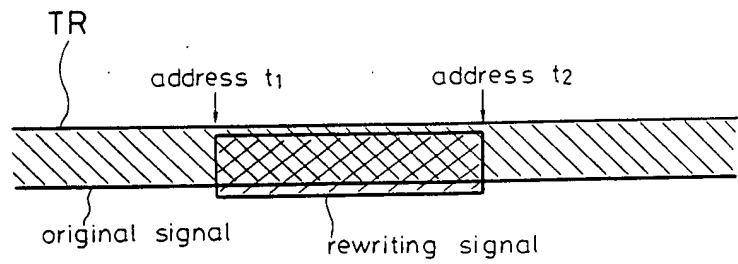
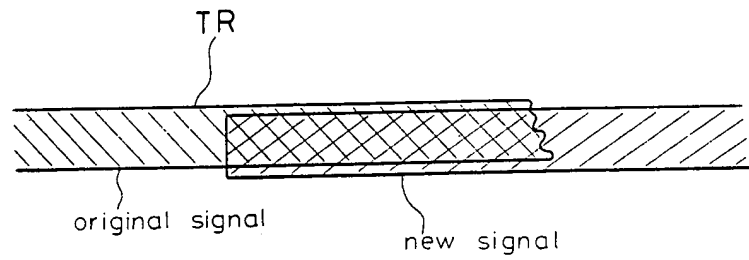
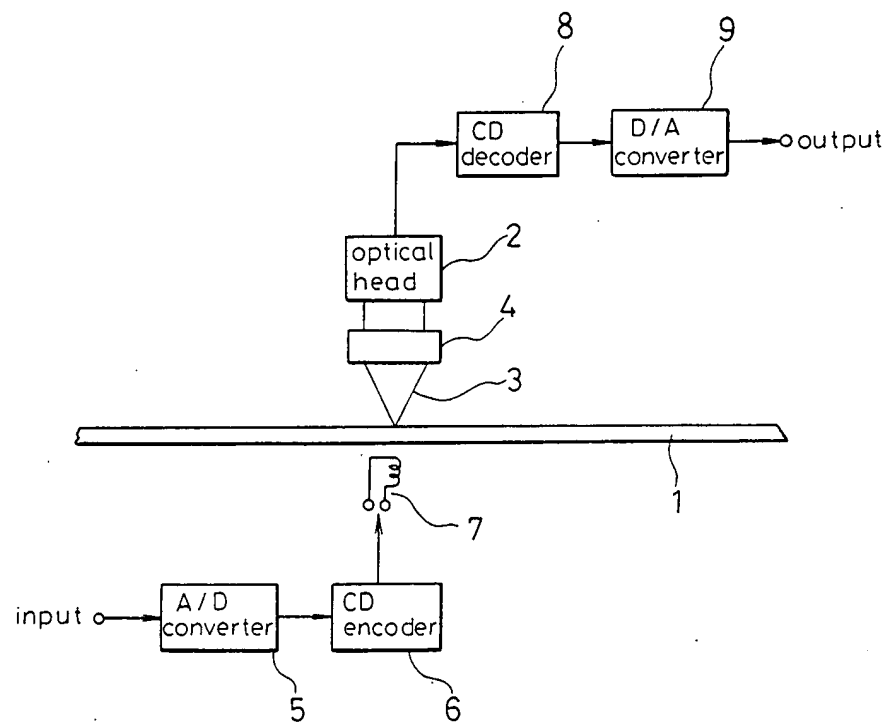


FIG. 7.



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FIG. 6.



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FIG. 8.

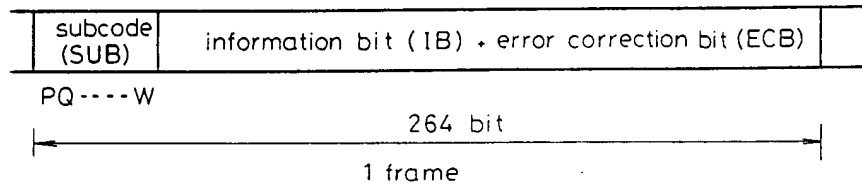
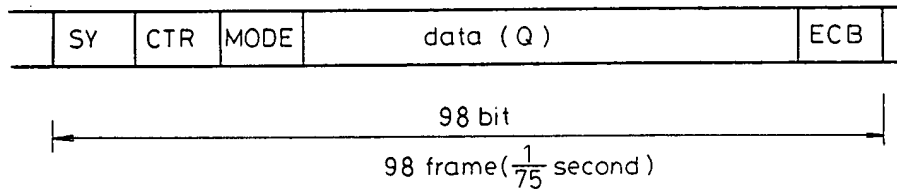


FIG. 9.



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FIG. 10.

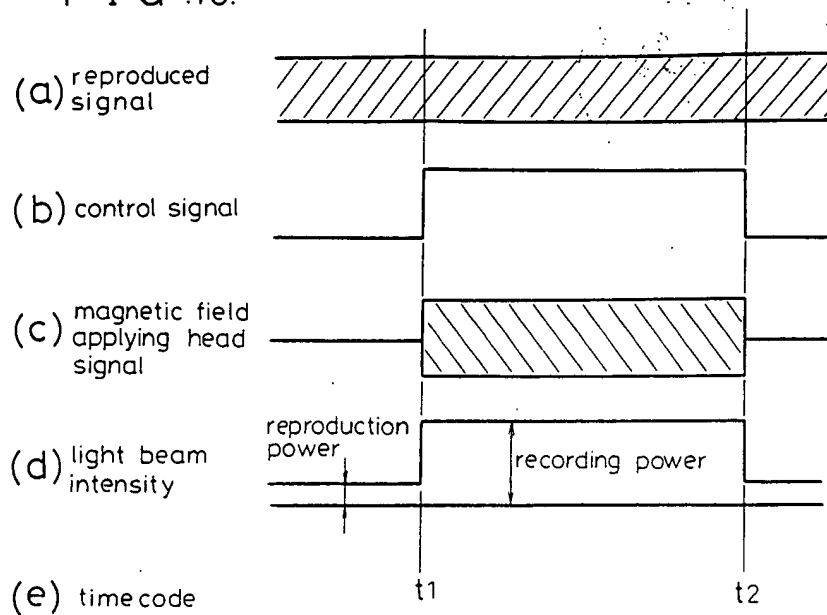


FIG. 11.

